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TERMINAL DISCLAIMER TO OBVIATE A DOUBLE PATENTING	Docket Number (Optional)
REJECTION OVER A PRIOR PATENT	BUR920000170US2
In re Application of: DAVID E. LACKEY	
Application No.: 10/706,538	
Filed: 11/12/2003	CID CITIT DECICNO
For: METHOD FOR INSERTION OF TEST POINTS INTO INTEGRATED LOGIC	CIRCUIT DESIGNS
International Business Machines Corporation The owner	
In making the above disclaimer, the owner does not disclaim the terminal part of any patent granted on the instant application that would extend to the expiration date of the full statutory term as defined in 35 U.S.C. 154 and 173 of the prior patent, as presently shortened by any terminal disclaimer, in the event that it later: expires for failure to pay a maintenance fee, is held unenforceable, is found invalid by a court of competent jurisdiction, is statutorily disclaimed in whole or terminally disclaimed under 37 CFR 1.321, has all claims canceled by a reexamination certificate, is reissued, or is in any manner terminated prior to the expiration of its full statutory term as presently shortened by any terminal disclaimer.	
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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the made with the highest that the second that such willful false statements may jeopardize the validation or any patent issued thereon.	
2. The undersigned is an attorney or agent of record. Robert M. Wall	9/16/2004
Signature	Date
Robert A. Walsh, Reg. No. Typed or printed name	20,310
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